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Overview of digital logic design ... Kime, Logic and Computer Design Fundamentals, Pearson/Prentice Hall, latest Edition Logic Synthesis and Verification Algorithms, Hachtel & Somenzi, Springer CMOS ...

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EK Koders, created by the National Program for Coders in collaboration with the Emirates Group, is part of plan to attract and train more than 100,000 coders ...

Emirates launches first coders club as the UAE seeks to lead digital revolution

A digital twin is a virtual model designed to accurately reflect a physical object. Digital twins use technologies like the internet of things, data streaming, 5G, etc., to gather, visualize and ...

Digital Transformation 's Next Wave: Creating a Mirrored World

The Hackett Group, Inc. (NASDAQ: HCKT) today announced the winners of its 2021 Digital Awards, which spotlight companies that are on the cutting edge of using digital transformation solutions, ...

The Hackett Group Announces 2021 Digital Award Winners

General Motors will invest more than \$71 million to set up its new design and technology center in Pasadena,

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California, replacing a smaller facility in North Hollywood where it has been based for the ...

GM Investing \$71 Million in New California Advanced Design Center

GM says moving the Advanced Design Center from North Hollywood to the new campus in Pasadena will increase the center's capacity and capability.

GM spending \$71M to move, enlarge California design campus

Law360 (June 24, 2021, 8:29 PM EDT) -- Intel told a Texas federal judge Wednesday its win over VLSI in a closely watched chip-patent trial should remain intact, saying VLSI didn't object at trial ...

Intel Fights To Keep No-Liability Patent Win Over VLSI

SASTRA and the Asia University and the Yuan Ze University in Taiwan have come together to offer deep domain expertise for the M.Tech aspirants in the field of electronics design and manufacturing.

TEPL and SASTRA to offer M.Tech in VLSI

It is an advanced, powerful, licensable 64-bit core that ... on silicon solutions with the innovative Nexperia? platform and VLSI Velocity? tool set. The company's Sea-of-IP? design methodology allows ...

Philips Semiconductors licenses MIPS Technologies' 64-Bit core in multimillion dollar deal for use in advanced digital consumer applications

VLSI Plus (www.vlsiplus.com) is a boutique IP house, specializing in digital video and, in particular, in IP

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cores complying with MIPI® CSI2 and CSI3 Camera Serial Interface standard. VLSI Plus is the ...

VLSI Plus announces an FPGA version of its 64-bit Second Generation MIPI CSI2 Receiver IISc, in partnership with TalentSprint, has launched PG level advanced certification programme in digital manufacturing and smart factories ...

IISc Launches Advanced Certification Programme in Digital Manufacturing and Smart Factories Law360 (June 21, 2021, 8:42 PM EDT) -- Intel Corp. asked the full Federal Circuit Monday to rehear its challenge to the Patent Trial and Appeal Board's policy of refusing to institute review of a ...

Intel Wants Full Fed. Circ. Redo Of VLSI Patent Appeals The Cadence digital full flow that has been optimized for use on UMC's 22ULP/ULL process technologies includes the Innovus™ Implementation System, Genus™ Synthesis Solution, Liberate™ ...

Cadence and UMC Collaborate on 22ULP/ULL Reference Flow Certification for Advanced Consumer, 5G and Automotive Designs As part of the initiative, students admitted to the two-year M.Tech degree in Very Large-Scale Integration (VLSI) design will complete ... internship in Taiwan's advanced semiconductor ...

Tata Electronics joins hands with SASTRA University Its technology is so advanced, Capital Economics said ... While the U.S. still leads the world in chip design and

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intellectual property with homegrown giants like Intel Corp., Nvidia Corp ...

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