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Programming the 80386: Crawford, John H., Gelsinger ...

Programming the 80386 by John H. Crawford, Patrick P. Gelsinger, January 1991, Sybex Inc edition,
Paperback in English

Programming the 80386 (January 1991 edition) | Open Library

Chapter 2 -- Basic Programming Model. This chapter describes the 80386 application programming environment as seen by assembly language programmers when the processor is executing in protected mode. The chapter introduces programmers to those features of the 80386 architecture that directly affect the design and implementation of 80386 applications programs.

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80386 Programmer's Reference Manual -- Chapter 02

INTEL 80386 PROGRAMMER'S REFERENCE MANUAL 1986 Page 1 of 421 INTEL 80386

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INTEL 80386 PROGRAMMER'S REFERENCE MANUAL 1986

Programming the 80386 This edition published in 1987 by SYBEX in San Francisco.

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However, 80386 supports 3 operating modes: real, protected and virtual real mode. Of the two modes of 80286 microprocessor, initially the 80286 was booted in real mode. However, to have better operating performance, separate software command is used to switch from the real mode to the protected mode.

What is 80386 Microprocessor? Features and Architecture of ...

The Intel 80386, also known as i386 or just 386, is a 32-bit microprocessor introduced in 1985. The first versions had 275,000 transistors and were the CPU of many workstations and high-end personal computers of the time. As the original implementation of the 32-bit extension of the 80286 architecture, the 80386

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instruction set, programming model, and binary encodings are still the common denominator for all 32-bit x86 processors, which is termed the i386-architecture, x86, or IA-32, depending o

Intel 80386 - Wikipedia

The programming model of the 8086 is considered to be program visible because its registers are used during application programming and are specified by the instructions. Figure below illustrates the programming model of 8086 microprocessor. ... In the 80386 and above, EBX also can address memory data. ECX (count)

Explain programming model of 8086.

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Programming the 80386 (豆瓣) - Douban

80386, 80486, and Pentium versions can be covered in complete detail. Through this approach, the operation of the microprocessor and programming with the advanced family members, along with interfacing all family members, provides a working and

THE INTEL MICROPROCESSORS

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An Intel 80386, for example, uses a completely different set of binary codes than a Motorola 68020, for designating equivalent functions. Unfortunately, there are no standards in place for microprocessor instructions. This makes programming at the very lowest level very confusing and specialized.

Microprocessor Programming | Principles Of Digital ...

Programming the 80386 Presents the assembly language programmer's view of the 80386 by developers of the chip itself. The Indispensable PC Hardware Book (4th Edition) This is the completely revised and updated fourth edition of this bestselling and award-winning title.

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In real mode the 80386 appears to programmers as a fast 8086 with some new instructions. Most applications of the 80386 will use real mode for initialization only. Virtual 8086 mode (also called V86 mode) is a dynamic mode in the sense that the processor can switch repeatedly and rapidly between V86 mode and protected mode.

80386 Programmer's Reference Manual -- Section 1.1

The 80386 contains a total of sixteen registers that are of interest to the applications programmer. As Figure 2-5 shows, these registers may be grouped into these basic categories: General registers. These eight 32-bit general-purpose registers are used primarily to contain operands for arithmetic and logical operations. Segment registers.

80386 Programmer's Reference Manual -- Section 2.3

The 80386 has instructions for referencing the segment registers (CS, DS, ES, SS, FS, GS). These instructions

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are used by applications programs only if systems designers have chosen a segmented memory model. The 80386 also has instructions for referring to the flag register. The flags may be stored on the stack and restored from the stack.

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