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The Motorola 68000 is a 16/32-bit complex instruction set computer

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microprocessor, introduced in 1979 by Motorola Semiconductor Products Sector. The design implements a 32-bit instruction set, with 32-bit registers and a 32-bit internal data bus. The address bus is 24-bits and does not use memory segmentation, which made it popular with programmers. Internally, it uses a 16-bit

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data arithmetic logic unit and two more
16-bit ALUs used mostly for addresses,
and has a 16-bit external data bus. For

Motorola 68000 - Wikipedia

various pieces of a microcomputer are
assembled to make a working system.

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microprocessors. They were most well known as the processors used in the early Apple Macintosh, the Sharp X68000, the Commodore Amiga, the Sinclair QL, the Atari ST, the Sega Genesis, the AT&T UnixPC, the Tandy Model 16/16B/6000, the Sun Microsystems Sun-1, Sun-2

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Professions ... Microprocessor Systems
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practical, providing the necessary detail to enable students to design actual, working systems.

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This book gives not only a complete details of Motorola 68000 family

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processors but also covers the basic required fundamentals of microprocessor design. It explains how interfacing, hardware and software purposes have been achieved in Motorola 68000 processor family.

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64-bit Microprocessor – INTEL CORE-2:

1.2GHz to 3GHz INTEL i7: 66GHz to

3.33GHz INTEL i5: 2.4GHz to 3.6GHz

INTEL i3: 2.93GHz to 3.33GHz We do

not have any 128-bit Microprocessor in

work at present one among the reasons for

this is that we are a long way from

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exhausting the 64 bit address space itself,
we use it a constant rate of roughly 2 ...

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Systems Design: 68000 Family ...

Chapter 1 introduces the Intel family of microprocessors with an emphasis on the

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microprocessor-based computer system:
its history, operation, and the methods
used to store data in a microprocessor-
based system. Number systems and
conversions are also included. Chapter 2
explores the programming model of the
microprocessor and system architecture.

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THE INTEL MICROPROCESSORS

The design was complete by 1970, and used a MOS-based chipset as the core CPU. The design was significantly (approximately 20 times) smaller and much more reliable than the mechanical systems it competed against, and was used in all of the early Tomcat models. This

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interfacing is moreover recommended to
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