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yet to be successfully integrated is the Power Amplifier (PA). The PA is usually the last active building block in a radio transmitter. Its function is to amplify the signal power up to the required level before it can be transmitted into the air. Due to several limitations of CMOS technology, designing a linear and efficient PA is a challenging task.

~~Efficiency Enhancement Techniques for CMOS RF Power Amplifiers~~

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Amplifiers A Complete Design Workflow 123. Hector Solar Ruiz Roc Berenguer Pérez Electronics and Communication Department Centre of Technical Research (CEIT) and University of Navarra (Tecnun) ... CMOS Complementary metal oxide semiconductor CS Common-source

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The RF power amplifier is a key component in a wireless transceiver and is considered by many as the design bottleneck in the transmitting chain. Linear CMOS RF Power Amplifiers for Wireless Applications addresses two fundamental aspects in RF power amplifier design for integration in CMOS technologies at 2.4, 3.7 and 5.2 GHz: efficiency enhancement and frequency agility.

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Abstract: This paper presents a highly linear differential cascode CMOS power amplifier (PA) with gate bias circuits in Common Source (CS) and Common Gate (CG) amplifiers. The proposed Class-D bias circuit at the gate of a CS amplifier injects a reshaped envelope signal only when the envelope signal is above a certain threshold voltage.

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