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This introductory book assumes minimal knowledge of the existence of integrated circuits and of the terminal behavior of electronic components such as resistors, diodes, and MOS and bipolar transistors. It presents to readers the basic information necessary for more advanced processing and design books. Focuses mainly on the basic processes used in fabrication, including lithography, oxidation, diffusion, ion implementation, and thin film deposition.

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10.1 Introduction In microelectronic fabrication, metals are used as electrically conducting materials for interconnects. The properties such as resistivity, lithography process compatibility, melting point, thermal

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expansion coefficient, diffusivity in an insulator, adhesion to the insulator, and interatomic distances (stress) of the metal must be considered when selecting an appropriate interconnect material.

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diameter of an atom, this feature size is
only 15 atoms wide. However, this narrow
width can probably can be achieved. 1.9 (3 x
10⁸ tubes)(0.5 W/tube) = 150 MW! I

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