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DON ' TS 1. Be regular to the lab. 2.

Follow proper Dress Code. 3. Maintain

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Silence. 4. Know the theory behind the experiment before coming to the lab. 5. Identify the different leads or terminals or pins of the IC before making connection. 6. Know the Biasing Voltage required for different families of IC ' s and connect

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Digital Electronics ...

Digital Electronic 1 Laboratory Manual . All readings should be within 10% of their marked voltages. Some interface devices in digital logic require both positive and negative polarity power supplies, and in those circuits, it is common to see a 0V

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Free ground reference. Turn off the trainer for the next measurement. 3. Variable Voltage Supply 3.1.

Digital Electronics 1 (ET181)

Laboratory Manual

Lab Manual: Digital Electronics Lab

(EE-224-F) DEPARTMENT OF

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ELECTRONICS & COMMUNICATION

ENGINEERING Page 12 An OR gate can have two or more inputs, its output is true if at least one input is true. NOT Gate (Inverter) Input A Output Q 0 1 1 0 Traditional symbol Truth Table It is also known as inverter.

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Digital Electronics Lab

This manual is intended for the
Second year students of CSE branches
in the subject of Digital Electronics .

This manual typically contains
practical/Lab Sessions related Digital
Electronics covering various aspects
related the subject to enhanced

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Understanding. Students are advised to thoroughly go through this manual rather than

Laboratory Manual DIGITAL
ELECTRONICS

Digital Electronics Lab (EELE 3121)

Syllabus Course title: Digital

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Electronics Lab Course Code:(EELE 3121) Prerequisite: Electronics (I) Lab, Digital Design Lab Laboratory
Experiments: The lab will cover the following experiments: Experiment 1: Introduction to Orcad. Experiment BJT Inverter. 2: Experiment 3: Diode-Resistor Logic (DRL) Gates.

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PROCEDURE: (a) With given equation
in SOP/POS forms first of all draw a K-
map. (b) Enter the values of the O/P
variable in each cell corresponding to

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its Min/Max term. (c) Make group of adjacent ones. (d) From group write the minimized equation. (e) Design the ckt. of minimized equation & verify the truth table.

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EXPERIMENT NO:1 Verification and interpretation of truth tables for AND, OR, NOT, NAND, NOR Exclusive OR (EX-OR), Exclusive NOR (EX-NOR) Gates.

LAB MANUAL (DIGITAL ELECTRONICS)

- amittal

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Electronic Device and Circuits - Unit 2
- Importan... Circuit Theory Question
Bank - (All Units) B.E., B.Tech. and
B.Arch. Regular and Part Time pr...
OBJECT ORIENTED PROGRAMMING
LAB MANUAL - (All Expe... Digital Lab
Manual (All Experiments) - CSE And IT
... Fourier Transform - Repeated

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No : 5 - Implementation of Tree
Travers...

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Experiments ...

digital electronic systems – Be able
to understand and apply Boolean

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Logic and algebra – a core competence in Computer Science –
Be able to understand and build state ...

- Keep up with lab work and get it ticked.
- Have a go at supervision questions plus any others your supervisor sets.
- Remember to try questions from past

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Digital Electronics Part I –
Combinational and Sequential ...
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FOR III SEMESTER B.E (E & C) (For
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theory behind the experiment before
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COMMUNICATIONS LAB List of Experiments: 1. PCM Generation and Detection. 2. Differential Pulse Code modulation. 3. Delta modulation. 4. Time Division Multiplexing of 2band Limited Signals. 5. Frequency Shift Keying: Generation and Detection. 6. Phase Shift Keying: Generation and

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DIGITAL COMMUNICATIONS LAB

This lab manual provides an introduction to digital logic, starting with simple gates and building up to state machines. Students should have a solid understanding of algebra as

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Free as a rudimentary understanding of basic electricity including voltage, current, resistance, capacitance, inductance and how they relate to direct current

Introduction to Digital Logic with
Laboratory Exercises

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LAB MANUAL – 15ECL38 III-

SEMESTER 2016-2017 Prepared by:

Reviewed by: Approved by: Mrs. A.

Deepa Mrs. Kavitha M V Dr. A.A. Powly

Thomas Assistant Professor Head of

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DEPARTMENT OF ELECTRONICS &
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User Manual. The user manual for
performing the experiments is given.
Click here to read the manual. 1.

Familiarization of ICs. Testing of AND

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Gate; Testing of NAND gate; Testing of OR Gate; Testing of NOR gate; Testing of XOR gate; 2. Combinational Circuits. Sum of Product; Product of Sum; Half Adder Using Logic Gates; 1 Bit Full Adder Using Logic Gates

Digital Electronics Laboratory

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Digital Electronics Circuits 2017 1 JSS
SCIENCE AND TECHNOLOGY
UNIVERSITY Digital Electronics
Circuits (EC37L) Lab in-charge: Dr.
Shankraiah Course outcomes: After
the completion of laboratory the
student will be able to, 1. Simplify,
design and implement Boolean

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Expression/half and full adders using
basic/universal gates. 2.

Digital Electronics Circuits

Objective of this laboratory manual is to teach students about electronics components, characteristics of semiconductor devices and design

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